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Docket No.: GR 98 P 2037

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By: Tom P. L.

Date: 3/28/01

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Martin Buck

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Applic. No. : 09/756,084

APR 02 2001

Filed : January 8, 2001

Technology Center 2600

Title : Circuit for Determining the Time Difference Between Edges
of a First Digital Signal and of a Second Digital Signal

Art Unit : 2631

INFORMATION DISCLOSURE STATEMENT

Hon. Commissioner of Patents and Trademarks,
Washington, D.C. 20231

Sir:

In accordance with 37 C.F.R. 1.98 copies of the following patents and/or publications are submitted herewith:

United States Patent No. 4,721,905 (Mehrgardt), dated January 26, 1988;

United States Patent No. 5,245,231 (Kocis et al.), dated September 14, 1993;

German Patent DE 195 06 543 C1 (Mende), dated May 9, 1996, clock generating circuit;

European Patent Application EP 0 576 150 A2 (Guo et al.), dated December 29, 1993;

European Patent Application EP 0 576 150 A3 (Guo et al.), dated December 29, 1993;

European Patent Application EP 0 639 004 A1 (Guo et al.), dated February 15, 1995;
International Publication WO 00/03258 (Buck), dated January 20, 2000.

If no translation of pertinent portions of any foreign language patents or publications mentioned above is included with the aforementioned copies of those applications, patents and/or publications, it is because no existing translation is readily available to the applicant.

Respectfully submitted,

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For Applicant

Date: 3/28/07

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